

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	104	(memory near3 cell near3 storage) near15 (voltage) near10 (current)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:03	
2	BRS	L2	2	(memory near3 cell near3 storage) near15 (voltage) near10 (current) near15 (resistance)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 20:58	
3	BRS	L4	1551	(metal near3 interconnect) near15 (substrate)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:02	
4	BRS	L5	0	3 and 4	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:00	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L3	206	(memory near3 cell) near15 (voltage) near10 (current) near15 (resistance)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2002/09/17 21:01	
6	BRS	L6	0	3 and 4	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2002/09/17 21:01	
7	BRS	L7	1932	(metal near5 interconnect) near15 (substrate)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2002/09/17 21:05	
8	BRS	L8	0	3 and 7	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2002/09/17 21:02	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
9	BRS	L9	2	(memory near3 cell near3 storage) near15 (chalcogenide)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:04	
10	BRS	L10	131	(memory near3 cell) near15 (chalcogenide)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:06	
11	BRS	L11	0	3 and 10	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:04	
12	BRS	L12	961	(interconnect near3 lines) near15 (substrate)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT; IBM- TDB	2002/09/17 21:06	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
13	BRS	L13	0	10 and 12	USPAT; US-PGPUB; ; EPO; JPO; DERWENT; IBM-TDB	2002/09/17 21:06	
14	BRS	L14	21	(memory near5 storage) near15 (chalcogenide)	USPAT; US-PGPUB; ; EPO; JPO; DERWENT; IBM-TDB	2002/09/17 21:07	
15	BRS	L15	0	12 and 14	USPAT; US-PGPUB; ; EPO; JPO; DERWENT; IBM-TDB	2002/09/17 21:07	
16	BRS	L16	0	7 and 14	USPAT; US-PGPUB; ; EPO; JPO; DERWENT; IBM-TDB	2002/09/17 21:07	

	U	1	Document ID	Title	Current OR	Pages	Issue Date
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US RE37259 E	Multibit single cell memory element having tapered contact	365/163	16	20010703
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5687112 A	Multibit single cell memory element having tapered contact	365/163	16	19971111